



AF/2818

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AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0363/P363Application No.
09/653,541Filing Date
August 31, 2000Examiner
Tu Tu V. HoArt Unit
2818

Applicant(s): Mark E. Tuttle

Invention: MAGNETIC SHIELDING FOR INTEGRATED CIRCUITS

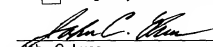
TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	82	- 96 =	0	x 18.00	0.00
Independent Claims	6	- 7 =	0	x 84.00	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

☒ Large Entity☐ Small Entity☒ No additional fee is required for this amendment.☐ Please charge Deposit Account No. _____ in the amount of \$ _____.
A duplicate copy of this sheet is enclosed.☐ A check in the amount of \$ _____ to cover the filing fee is enclosed.☐ Payment by credit card. Form PTO-2038 is attached.☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 04-1073
as described below. A duplicate copy of this sheet is enclosed.☒ Credit any overpayment.☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.
John C. Luce
Attorney Reg. No.: 34,378

Dated: December 30, 2002

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Docket No.: M4065.0363/P363
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Mark E. Tuttle

Application No.: 09/653,541

Group Art Unit: 2818

Filed: August 31, 2000

Examiner: Tu Tu V. Ho

For: MAGNETIC SHIELDING FOR
INTEGRATED CIRCUITS

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AMENDMENT AFTER FINAL REJECTION

ATTN: Box AF
Commissioner for Patents
Washington, DC 20231

Dear Sir:

Responsive to the Office Action dated August 28, 2002, please amend the
above-captioned application as follows:

IN THE CLAIMS:

Rewrite claims 1-3 as follows:

1. (Amended) An integrated circuit structure comprising:

a chip carrier;

at least one integrated circuit chip containing structures which may be affected
by external magnetic fields, said integrated circuit chip having a front surface and a back
surface, said front surface being supported by said chip carrier, and wherein said structures